
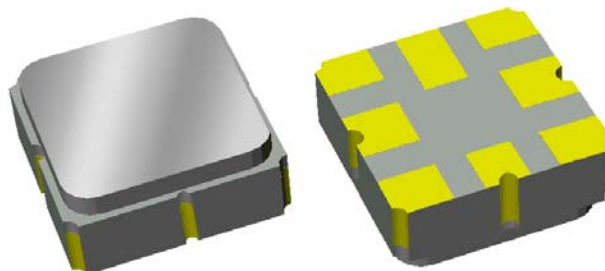


Preliminary Data Sheet

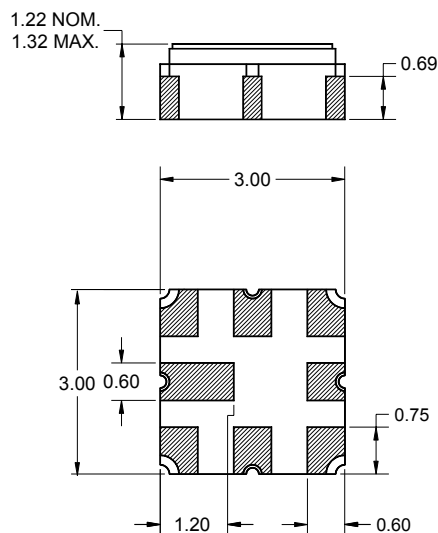
Features

- For FRS applications
- Usable bandwidth 6 MHz
- Low loss
- High attenuation
- No impedance matching required for operation at 50 Ω
- Single-ended operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



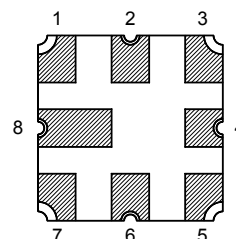
Package

Surface Mount 3.00 x 3.00 x 1.22 mm



Pin Configuration

Bottom View



Pin No.	Description
2	Input
6	Output
1,3,4,5,7,8	Case ground

Dimensions shown are nominal in millimeters
 All tolerances are ± 0.15 mm except overall
 length and width ± 0.10 mm

Body: Al_2O_3 ceramic
 Lid: Kovar, Ni plated
 Terminations: Au plating 0.5 - 1.0 μ m,
 over a 2 - 6 μ m Ni plating

Preliminary Data Sheet

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

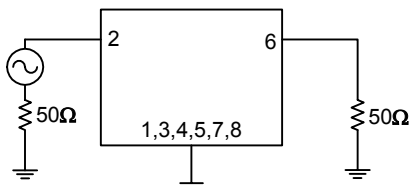
Parameter ⁽³⁾	Minimum	Typical	Maximum	Unit
Center Frequency	-	465	-	MHz
Maximum Insertion Loss 462 - 468 MHz	-	1.43	1.7	dB
Amplitude Variation 462 - 468 MHz	-	0.16	0.5	dB
Attenuation 421.6 MHz	50	70	-	dB
Minimum Rejection 100 - 441 MHz	48	61	-	dB
441 - 445 MHz	40	62	-	dB
485 - 502 MHz	24	29	-	dB
502 - 510 MHz	40	47	-	dB
510 - 1000 MHz	40	53	-	dB
Input/Output VSWR 462 - 468 MHz	-	1.42	1.6	-
Source Impedance ⁽⁴⁾	-	50	-	Ω
Load Impedance ⁽⁴⁾	-	50	-	Ω

Notes:

1. All specifications are based on the test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

Test Circuit:

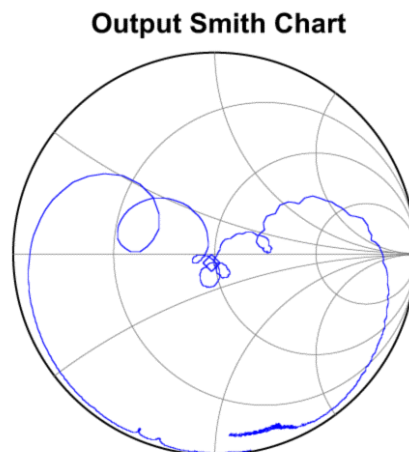
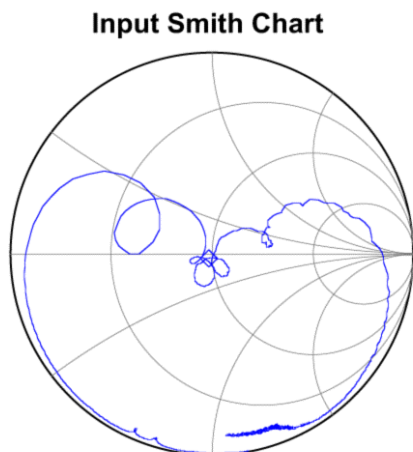
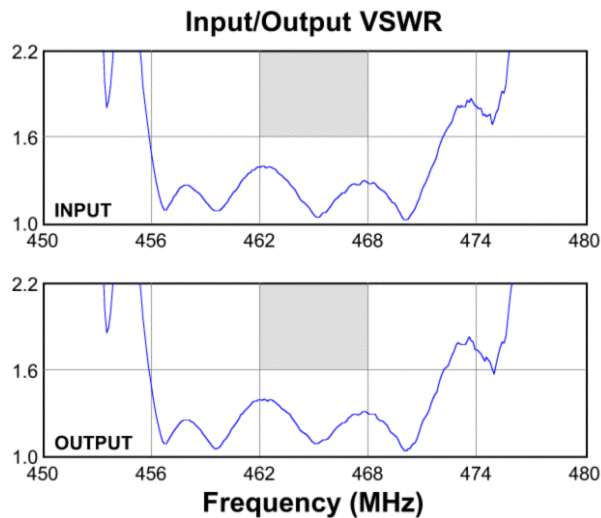
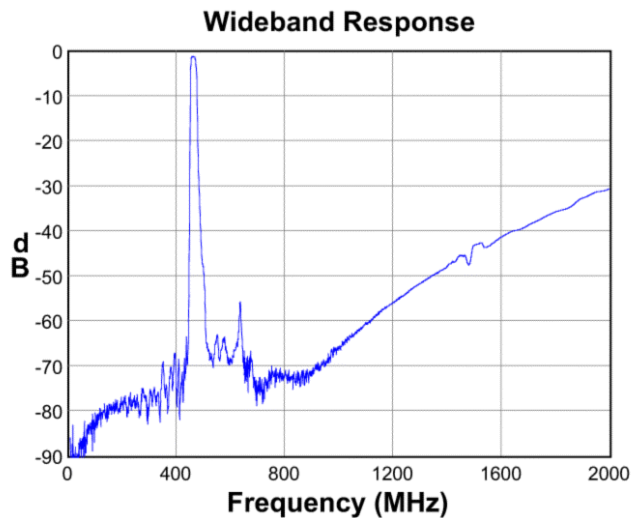
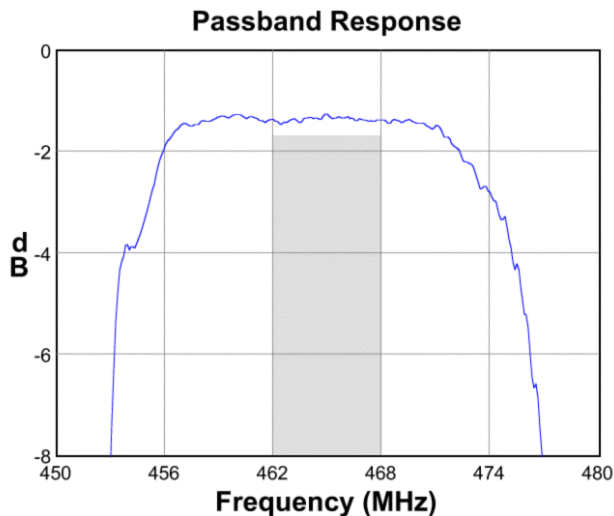
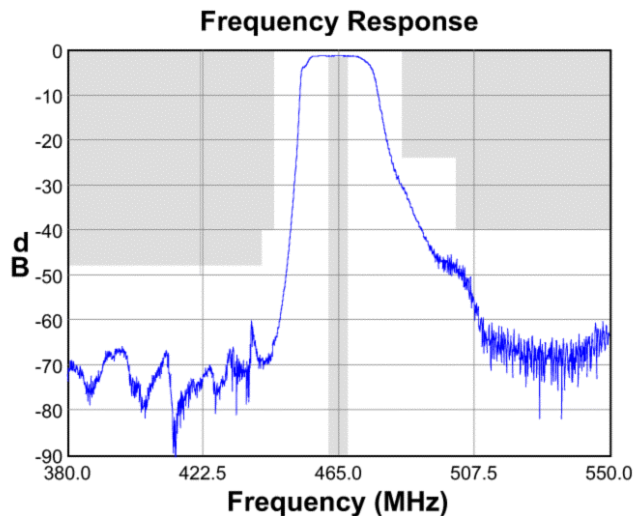
50 Ω
Single-ended



No impedance matching
required

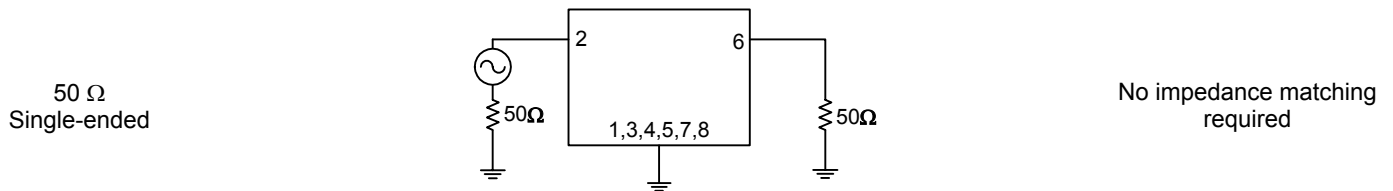
Preliminary Data Sheet

Typical Performance (at +25°C)

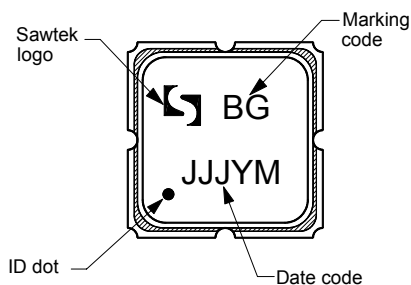


Preliminary Data Sheet

Matching Schematics

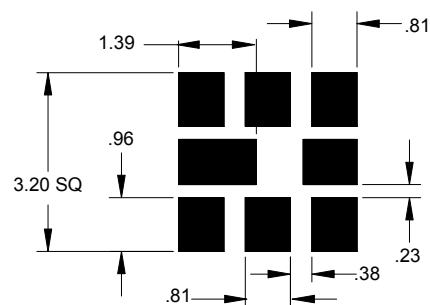


Marking



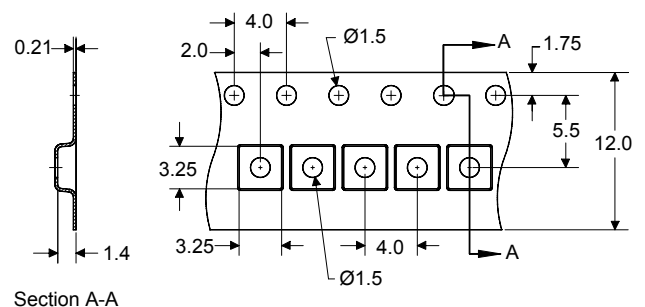
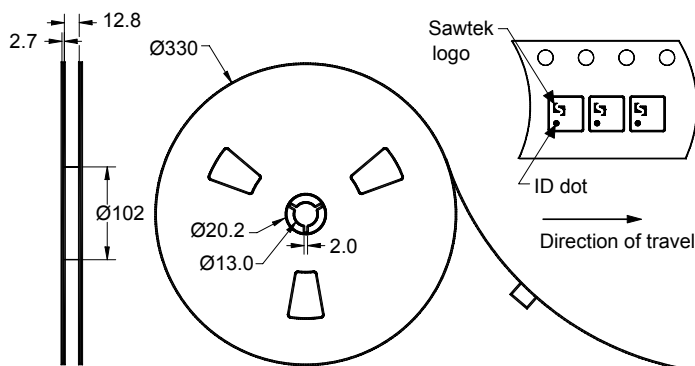
The date code consists of: JJJ = Julian day,
Y = last digit of year, M = manufacturing site code

PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel



Dimensions shown are nominal in millimeters
Packaging quantity: 5000 units/reel

Preliminary Data Sheet

Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-40	+85	°C

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD)
- Avoid ultrasonic exposure



RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS)



Solderability

- Compatible with JEDEC J-STD-020C **Pb**-free process, **260°C** peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)[Qualification Flowchart](#)[Soldering Profile](#)[S-Parameters](#)[RoHS information](#)[Other Technical Information](#)

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