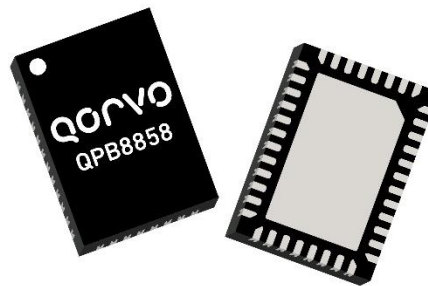


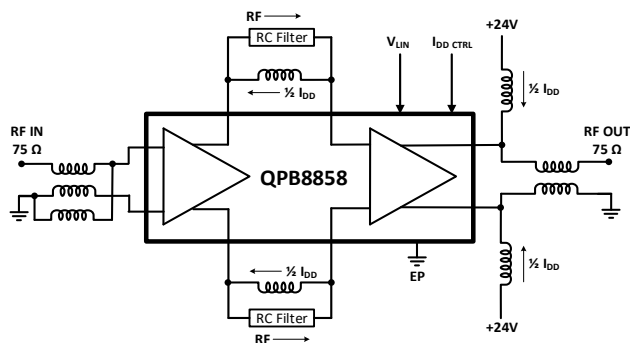
Product Overview

The QPB8858 is a GaAs pHEMT/MESFET 75-ohm push-pull RF amplifier IC featuring over 34dB of flat gain and low noise. This IC is designed to support DOCSIS 3.1 applications up to 1218MHz using a single 24V supply. The QPB8858 offers low noise and low distortion at high efficiency consuming only 7W in a 5x7 QFN package. Its compact size and low Θ_{jc} enable integration that is ideally suited for hybrid module applications.



40P 5x7 QFN Package

Functional Block Diagram



Key Features

- High Gain: 34dB @ 1218MHz
- Adjustable Bias
- 47 - 1218 MHz BW
- 47dBmV/ch flat
- Low Noise: 4dB
- Excellent Composite Distortion
- pHEMT / MESFET device technologies
- Compact Size: 40P 5x7 QFN
- Power Consumption (24V, 290mA - 7W)

Applications

- DOCSIS 3.1
- Broadband CATV hybrid modules
- Head End CMTS Equipment
- 75-ohm amplifiers

Ordering Information

Part No.	Description
QPB8858SQ	Sample bag with 25 pieces
QPB8858SR	7" Reel with 100 pieces
QPB8858TR13	13" Reel with 2500 pieces
QPB8858EVb	47 – 1218MHz PCBA

Absolute Maximum Ratings

Parameter	Rating
Supply Voltage (V_{DD})	+30V (5min)
Supply Current (I_{DD})	350mA
Maximum Input Level (single tone)	+70dBmV
Operating Temperature Range	-40 to +100°C
Storage Temperature Range	-65 to +150°C
Maximum Junction Temperature	+150°C

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

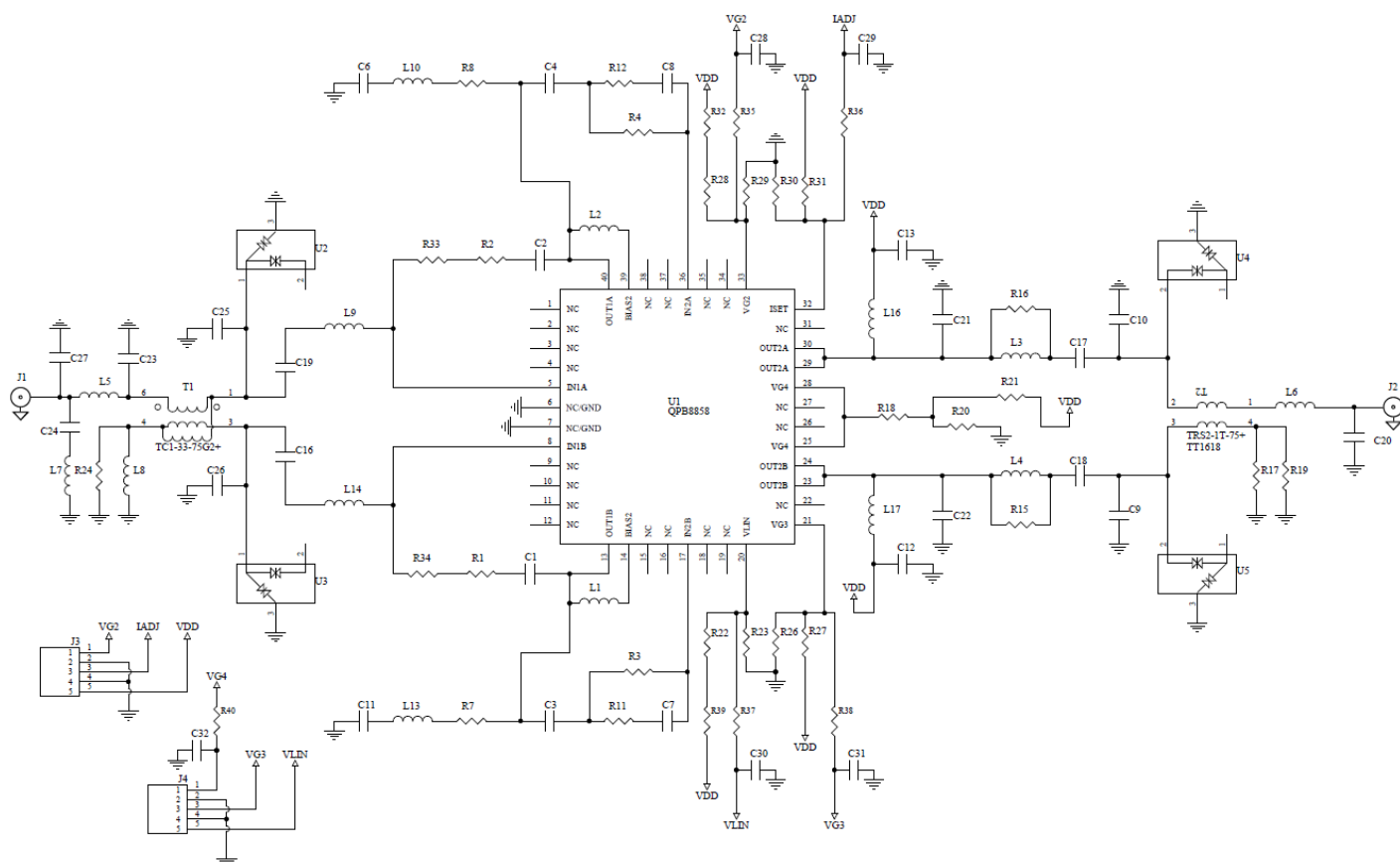
Electrical Specifications – 24V

Parameter	Condition ⁽¹⁾	Min	Typ	Max	Unit
Supply Voltage (V_{DD})			24		V
Supply Current (I_{DD})	V_{DD} total current		290		mA
Frequency Range		47		1218	MHz
Gain ²	1218MHz		34		dB
Gain Flatness	Max. deviation from line using least squares fit from 47 to 1218 MHz		±0.5		dB
Gain Slope	Gain(1218MHz) - Gain(50MHz)		1.0		dB
Reverse Isolation			-45		dB
Input Return Loss			18		dB
Output Return Loss			18		dB
Noise Figure			4		dB
CSO	80 NTSC + 111 QAM (-6dB offset), 47dBmV/ch out, 0dB tilt		-70		dBc
CTB			-75		dBc
CCN			65		dB
OIP2	Low band: 225MHz, 275.5MHz, 15dBm/tone		85		dBm
	High band: 1100MHz, 1150.5MHz, 15dBm/tone		85		dBm
OIP3	Low band: 225MHz, 275.5MHz, 15dBm/tone		52		dBm
	High band: 1100MHz, 1150.5MHz, 15dBm/tone		48		dBm
Output P1dB	1218 MHz		28		dBm
Thermal Resistance	Θ_{JB} (Junction to backside of QFN)		7.5		°C/W

Notes:

1. Typical performance at these conditions: Temp = +25°C, V_{DD} = +24V, 75Ω system, Full band unless otherwise noted
2. Gain (or Tilt) can be modified between stages by approximately -4dB (S_{21} = 30dB).

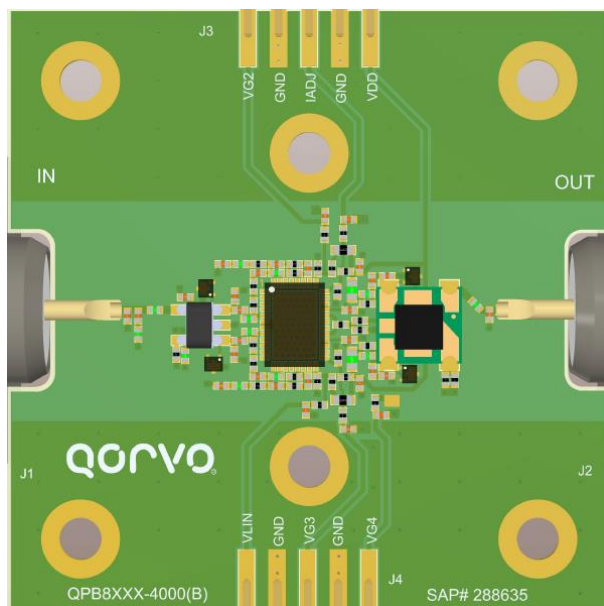
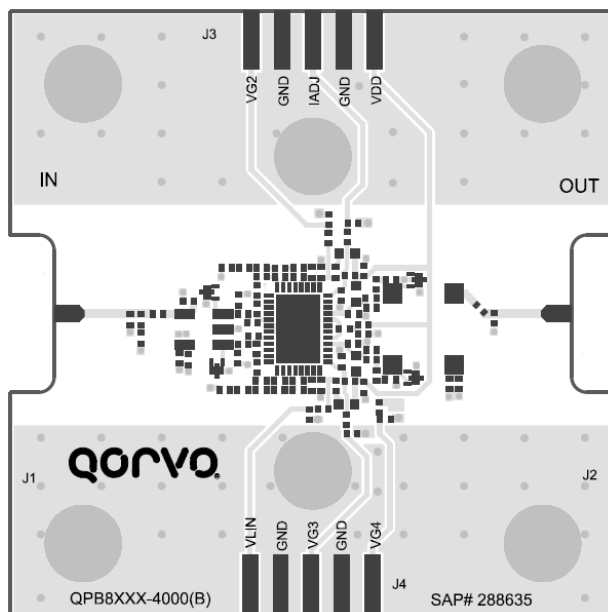
Evaluation Board Schematic 47 – 1218MHz



Evaluation Board Bill of Materials

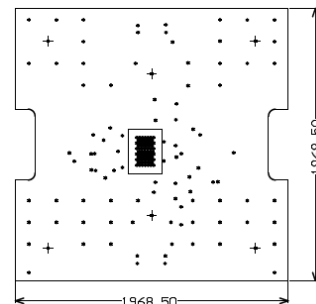
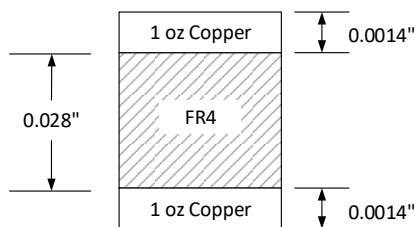
Reference Designator	Description	Manufacturer	PART #
U1	1.2GHz, 34dB Push-Pull MMIC	Qorvo	QPB8858
C1,C2,C3,C4,C12,C13, C16,C17,C18,C19,C29, C30	CAP, 0.01uF, ±10%, 50V, X7R, 0402	MURATA	GRM155R71H103KA88D
C26,C20,C24,C9	CAP0402, 0.5pF, ±0.1pF, 50V, C0G, HIQ	MURATA	GJM1555C1HR50BB01D
C7,C8	CAP, 20pF, 2%, 50V, HI-Q, 0402	MURATA	GJM1555C1H200GB01D
C6,C11	CAP, 470pF, 5%, 50V, C0G, 0402	MURATA	GRM1555C1H471JA01D
R18	RES, 10 OHM, 1%, 1/16W, 0402	Panasonic	ERJ-2RKF10R0X
R31	RES, 5.60K ohm, 1%, 1/16W, 0402	Vishay	CRCW04025K60FKED
R22	Res, 30.1K, 1/16W, 1%, 04 02	KOA	RK73H1ETTP3012F
R19,R11,R12	RES, 0 OHM, 5%, 1/10W, 0402	Kamaya	RMC1/16SJPTH
R3,R4	RES, 15 OHM, 5%, 1/16W, 0402	Kamaya	RMC1/16S-150JTH
R33,R34	RES, 200 OHM, 1%, 1/16W, 0402	Panasonic	ERJ-2RKF2000X
R21	RES, 10K, 1%, 1/16W, 0402	Panasonic	ERJ-2RKF1002X
R20	RES, 8.2K, +/-1%, 1/10W, 0402	Panasonic	ERJ-2RKF8201X
R27,R28	RES, 47K, 1%, 1/16W, 0402	Yageo	RC0402FR-0747KL
R1,R2	RES, 270 OHM, 5%, 1/16W, 0402	Panasonic	ERJ-2GEJ271
R36,R37	RES, 1.2K, 5%, 1/16W, 0402	Panasonic	ERJ-2GEJ122
R32,R39	RES, 0 OHM, 0603	Kamaya	RMC1/16JPTP
R23	RES, 2K, 5%, 1/16W, 0402	Panasonic	ERJ-2GEJ202
R7,R8	160 OHM,5%,1/16W,0402, LEAD FREE	KOA	RK73B1ETTP161J
L1,L2	IND, 470nH, ±5%, 310mA, 650mHZ, 0402	Coilcraft	0402AF-471XJLW
L9,L14	IND, 2.2nH, +/-0.3nH, M/L, 0402	MURATA	LQG15HN2N2S02D
L8	IND, 1.5nH, +/-0.3nH, M/L, 0402	MURATA	LQG15HN1N5S02D
L3,L4,L7	IND, 8.2nH, 5%, M/L, 0402	MURATA	LQG15HN8N2J02D
L10,L13	IND, 47nH, 5%, M/L, 0402	MURATA	LQG15HN47NJ02D
L5,L6	IND, 3.3nH, +/-0.1nH, T/F, 0402	MURATA	LQP15MN3N3B02D
L16,L17	IND, 2.2uH, 20%, 0.36A, 0.7mm, W/W, 0603	TAIYO YUDEN	BRL1608T2R2M
T1	XFMR, SMT, 75 OHM, CD542, 1:1 BALUN, TC1	Mini Circuits	TC1-33-75G2+
T2	TRANS, 75ohm, 30mA, 4.5-3000MHz	Mini Circuits	TRS2-1T-1+
R25,R26,R29, R30,R38,R40, R35	NO LOAD - 0402		
U2,U3,U4,U5,C10,C21, C22,C23,C25,C27,C28, C31,C32, R15,R16,R17,R24	NO LOAD - 0402		

Evaluation Board Assembly Drawing

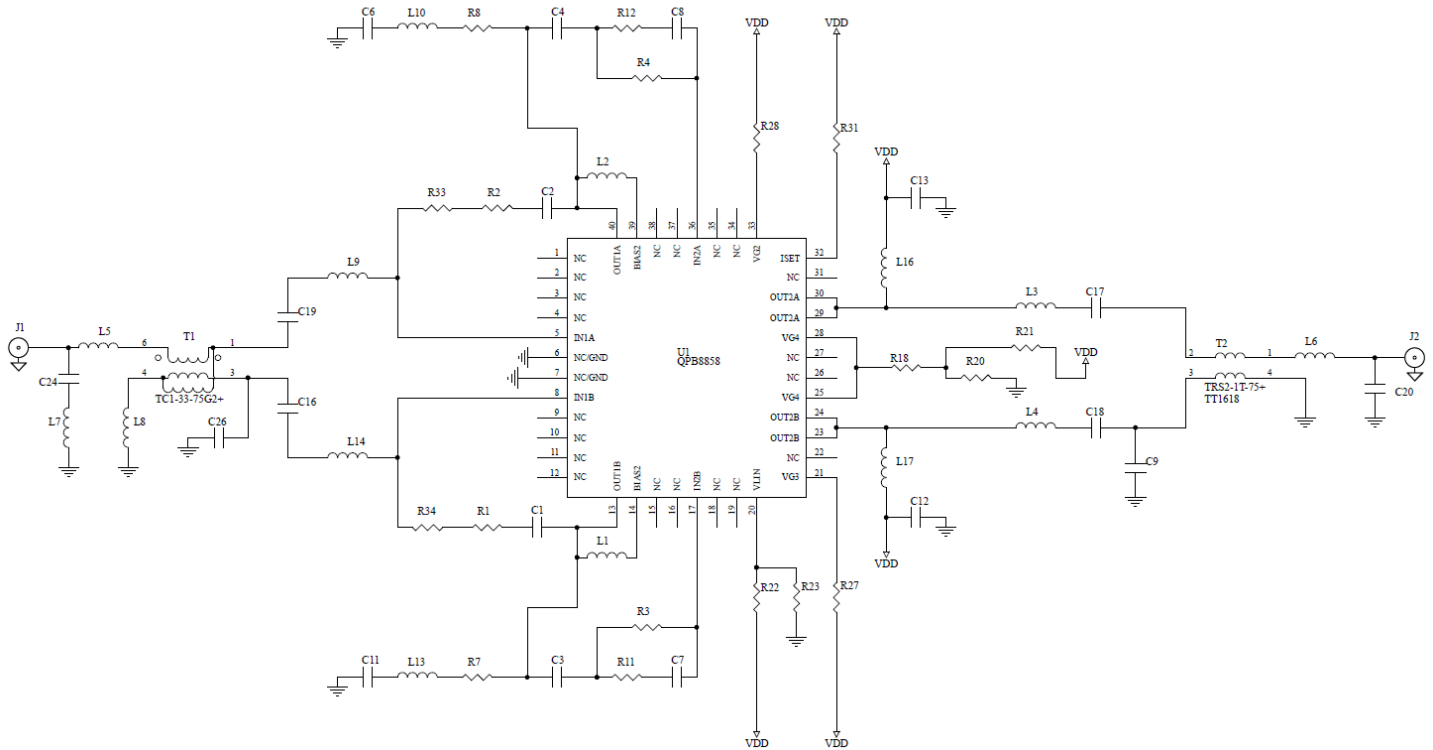


EVB PCB Material and Stack-up

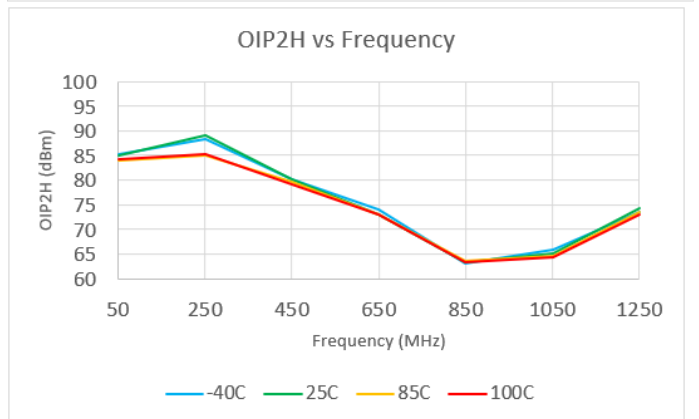
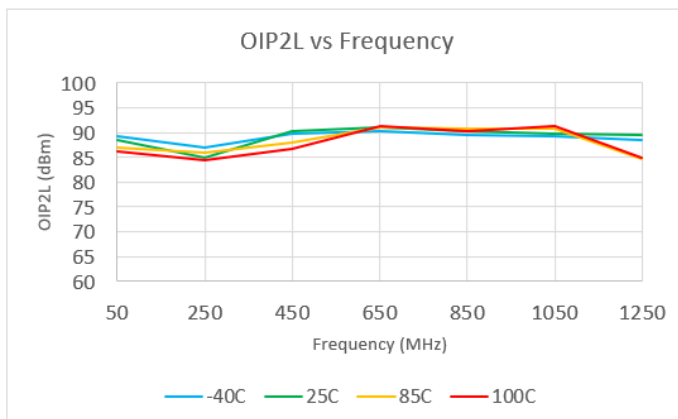
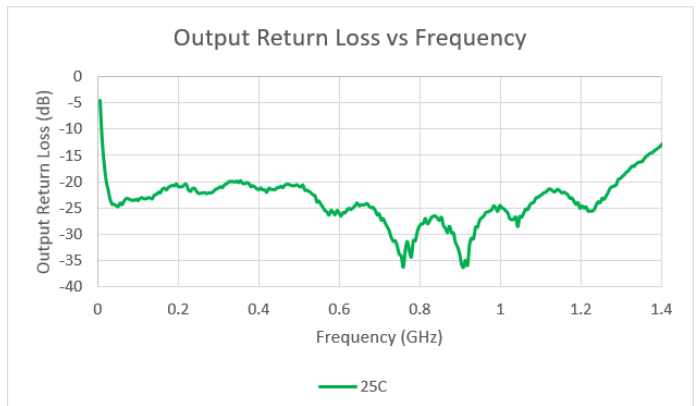
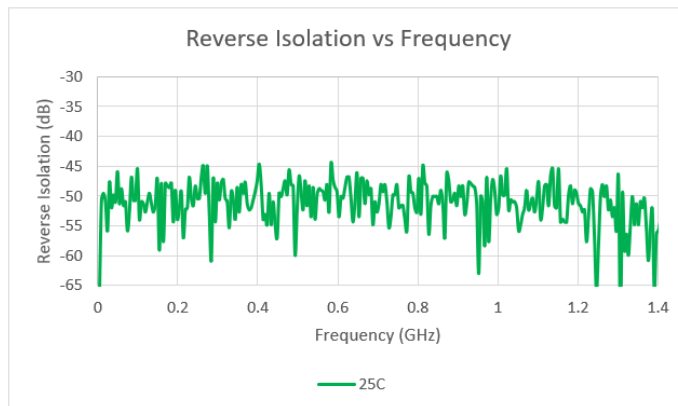
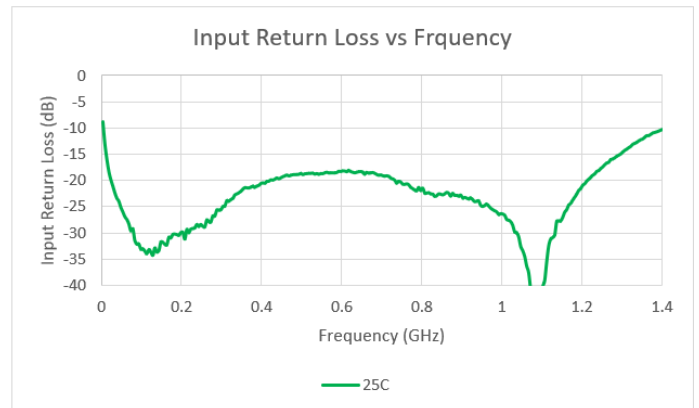
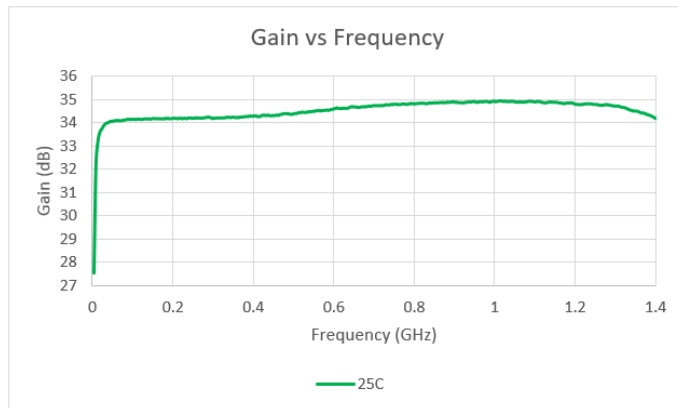
Board Material: 0.028" FR4, $\epsilon_r=4.2$
 Plating: 1oz Copper
 Board Dimension: 1.9685" x 1.9685"



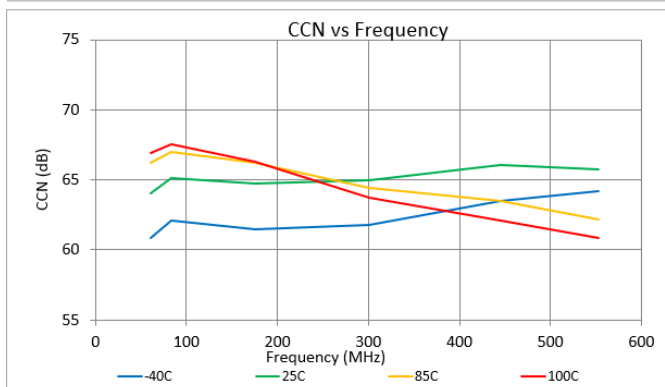
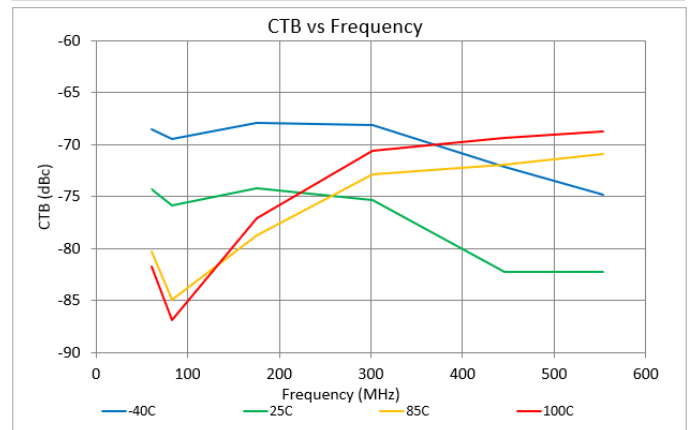
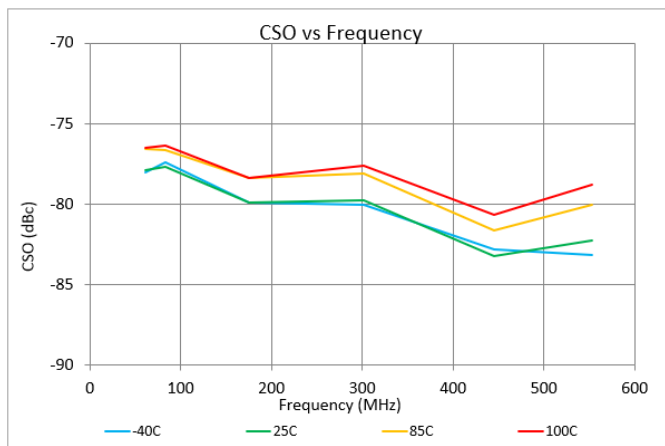
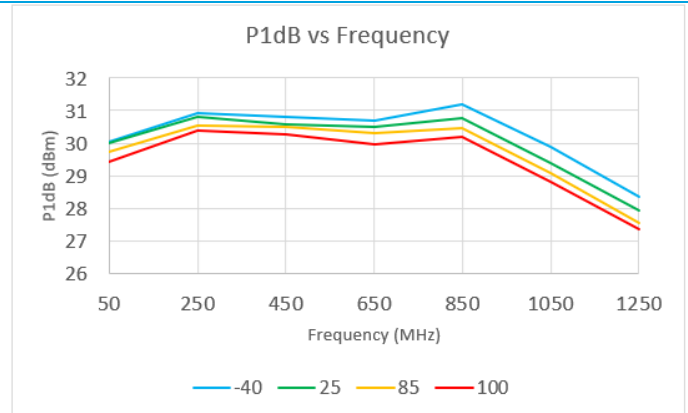
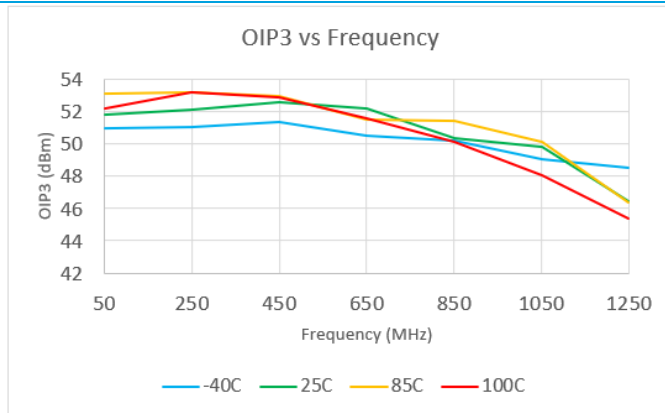
Typical Application Schematic, 47MHz – 1218MHz



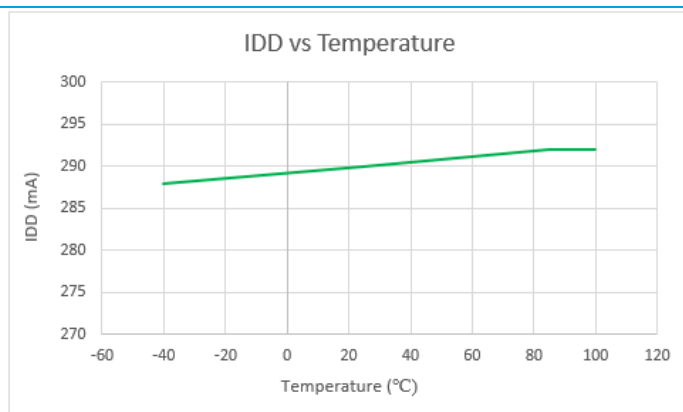
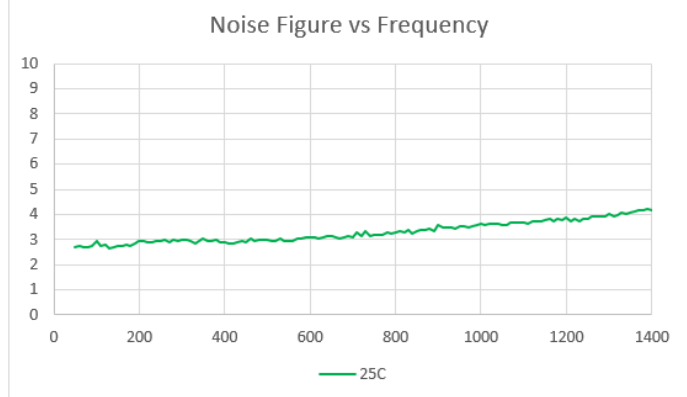
Performance Data 24V



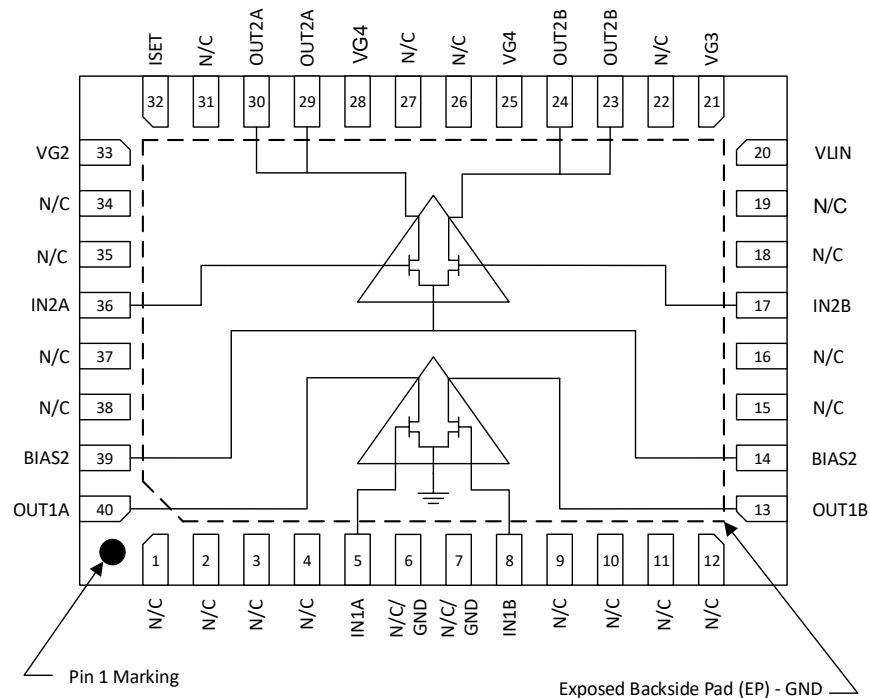
Performance Data 24V



Performance Data 24V



Pin Configuration and Description



Top View

Pad No.	Label	Description
5	IN1A	RF input 1A
8	IN1B	RF input 1B
13	OUT1B	RF output 1B
14, 39	BIAS2	2nd Stage virtual ground
17	IN2B	RF input 2B
20	VLIN	Linearizer current set
21	VG3	VG3 adjust
23, 24	OUT2B	RF output 2B
25, 28	VG4	VG4 set
29, 30	OUT2A	RF output 2A
32	ISET	IDD set
33	VG2	VG2 adjust
36	IN2A	RF input 2A
40	OUT1A	RF output 1A
1, 2, 3, 4, 9, 10, 11, 12, 15, 16, 18, 19, 22, 26, 27, 31, 34, 35, 37, 38	N/C	No connect
6, 7	N/C/GND	No connect or ground
Backside Paddle	GND	Ground. Use recommended via pattern to minimize inductance and thermal resistance. See PCB Mounting Pattern for suggested footprint.

Technical drawing of a microchip showing three views: TOP VIEW, SEATING PLANE, and BOTTOM VIEW.

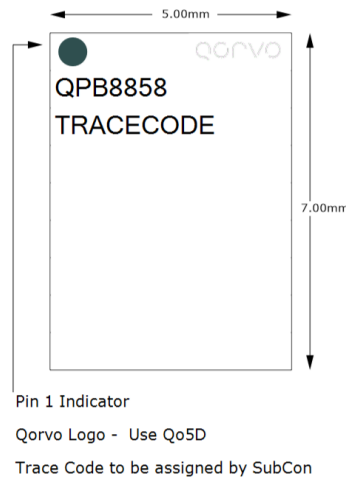
TOP VIEW: Shows a rectangular chip with dimensions 5.00 ± 0.10 (width) and 7.00 ± 0.10 (height). A "PIN 1 INDICATOR" is located in the top-left corner. Datum A is at the top edge, and datum B is at the right edge. A feature control frame at the bottom center specifies a flatness tolerance of 0.10 C A B.

SEATING PLANE: A side view showing the chip's profile with a maximum thickness of 0.850 ± 0.05 . It indicates two different thicknesses: 0.05 and 0.203 .

BOTTOM VIEW: Shows the underside of the chip with dimensions 5.00 (width) and 5.40 (height). It features a central square area with rounded corners ($R0.08$) and a perimeter of pins. Pin dimensions include 0.300 , 3.40 , 0.50 , 40×0.25 , 40×0.50 , and (3.50) . A feature control frame at the bottom center specifies a flatness tolerance of 0.10 C. A callout for "PIN # 1 I.D. (0.50X45°)" points to a specific pin. A feature control frame at the bottom left specifies a circular runout tolerance of 0.10 A B C and a circular runout tolerance of 0.05 C.

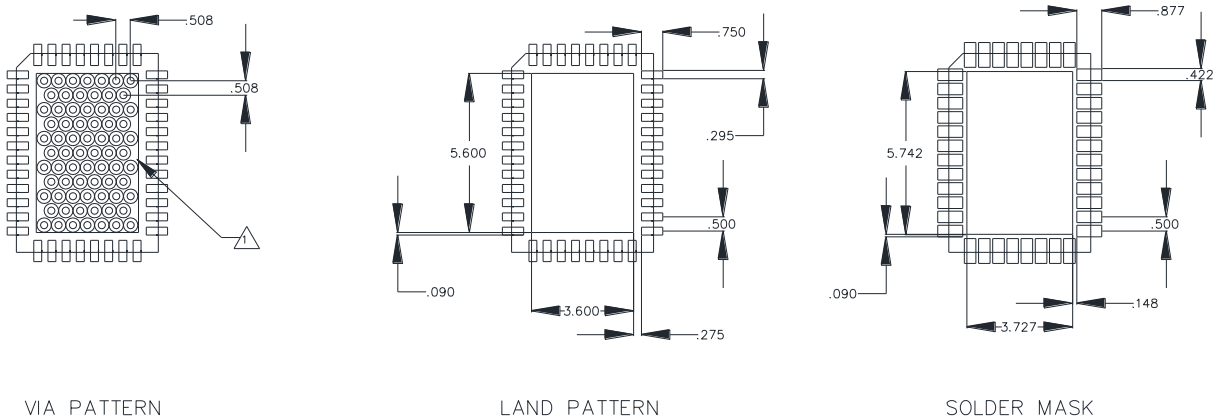
1. Dimensions in millimeters

Package Marking



1. Dimension and tolerance formats conform to ASME Y14.4M-1994.
2. The terminal #1 identifier and terminal numbering conform to JESD 95-1 SPP-012.
3. Co-planarity applies to the exposed ground/thermal pad as well as the contact pins.
4. Package body length/width does not include plastic flash protrusion across mold parting line.

Recommended Mounting Pattern



NOTES:

1. GROUND/THERMAL VIAS AND MOUNTING HOLES ARE ESSENTIAL FOR THE PROPER DEVICE PERFORMANCE. DO NOT OMIT. VIAS SHOULD USE A .35mm (#80/.0135") DIAMETER DRILL AND HAVE A FINAL, PLATED THRU DIAMETER OF .25mm (.010").
2. TO ENSURE RELIABLE OPERATION, DEVICE GROUND PADDLE-TO-GROUND PAD SOLDER JOINT IS CRITICAL. NO SOLDER MASK ON BACKSIDE OF PCB IN HEAT SINK CONTACT AREA.
3. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.

- Ensure good package backside paddle solder attach for reliable operation and best electrical performance.
- Place mounting screws near the part to fasten a back-side heat sink.
- Do not apply solder mask to the back side of the PC board in the heat sink contact region.
- Ensure that the backside via region makes good physical contact with the heat sink.

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	1B	ANSI / ESDA / JEDEC JS-001
ESD – Charged Device Model (CDM)	C3	ANSI / ESDA / JEDEC JS-002
MSL – Moisture Sensitivity Level	MSL3	IPC / JEDEC J-STD-020



Caution!
ESD-Sensitive Device

Solderability

Compatible with both lead-free (260°C max. reflow temp.) and tin/lead (245°C max. reflow temp.) soldering processes.

Solder profiles available upon request.

Contact plating: NiPdAu

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

Tel: 1-844-890-8163

Web: www.qorvo.com

Email: customer.support@qorvo.com

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