



QPA4263C

50 – 4000 MHz Cascadable SiGe Amplifier

General Description

The QPA4263C is a high performance SiGe MMIC amplifier utilizing a Darlington configuration with a patented active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 3V supply, the QPA4263C does not require a dropping resistor as compared to typical Darlington amplifiers. The QPA4263C is designed for high linearity 3V gain block applications that require small size and minimal external components. It is internally matched to 50Ω.



6 Lead SOT-363 Package

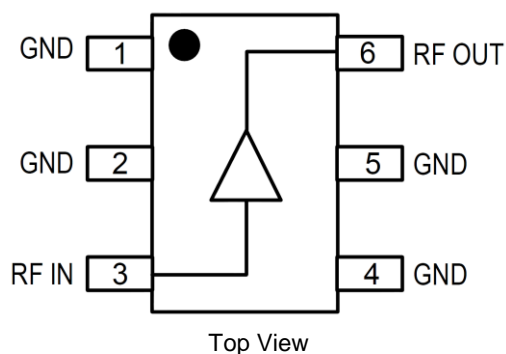
Product Features

- 50 to 4000 MHz Operation
- Single fixed 3V Supply
- Patented Self-Bias Circuitry
- 14 dB Gain at 1950 MHz
- 27.7 dBm OIP3 at 1950 MHz
- Cascadable 50 Ω
- Low Thermal Resistance Package

Applications

- Instrumentation
- Repeaters
- Boosters
- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Functional Block Diagram



Ordering Information

Part No.	Description
QPA4263CTR7	3000 pieces on a 7" reel (standard)
QPA4263CSQ	25 Piece Sample Bag
QPA4263CSR	100 Pieces on 7" Reel
QPA4263CPCK401	850 MHz, EVB with 5 Piece Sample Bag

Absolute Maximum Ratings

Parameter	Rating
Storage Temp	-55 to +150 °C
Device Voltage (V_D)	+4 V
Device Current (I_D)	110 mA
RF Input Power	+12 dBm

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Operating Temperature	-40		+105	°C
Junction Temperature (T_J)			+125	°C
Device Operating Voltage		+3.0		V

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

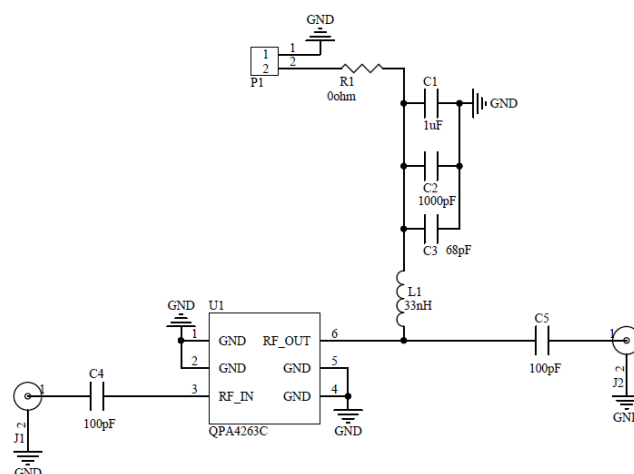
Electrical Specifications

Parameter	Conditions	Min	Typ	Max	Units
Small Signal Gain	850 MHz		14.1		dB
	1950 MHz		14		dB
	2400 MHz		13.7		dB
Output Power at 1dB Compression	850 MHz		14.6		dBm
	1950 MHz		14.2		dBm
	2400 MHz		14.2		dBm
Output Third Intercept Point	500 MHz ⁽²⁾		+31.5		dBm
	850 MHz ⁽²⁾		+31.4		dBm
	1950 MHz ⁽¹⁾		+27.7		dBm
	2400 MHz ⁽²⁾		+27.8		dBm
Input Return Loss	850 MHz		-29		dB
	1950 MHz		-30		dB
	2400 MHz		-37		dB
Output Return Loss	850 MHz		-19		dB
	1950 MHz		-18		dB
	2400 MHz		-15.6		dB
Reverse Isolation	850 MHz		-18.6		dB
	1950 MHz		-19.5		dB
	2400 MHz		-19.7		dB
Noise Figure	850 MHz		3.6		dB
	1950 MHz		3.8		dB
	2400 MHz		4.1		dB
Device Operating Current			55		mA
Thermal Resistance, θ_{jc}			96		°C/W

Notes:

- Test conditions unless otherwise noted: $V_D = +3.0$ V, $I_D = 55$ mA Typ., OIP3 Tone Spacing=1 MHz, P_{OUT} per tone = 0 dBm, 50 Ω system, Temp = +25 °C
- Test conditions unless otherwise noted: $V_D = +3.0$ V, $I_D = 55$ mA Typ., OIP3 Tone Spacing=1 MHz, P_{OUT} per tone = -6 dBm, 50 Ω system, Temp = +25 °C

Evaluation Board and Schematic – 850 MHz Application Circuit



Bill of Material – 850 MHz Application Circuit

Reference	Value	Description	Manufacturer	Part Number
n/a	n/a	PCB, QPA4263C	Qorvo	QPAXX63X-410(A)
C1	1 uF	CAP, 10%, 25V, X7R, 1206	Murata Electronics	GRM31MR71E105KA01L
C2	1000 pF	CAP, 10%, 50V, X7R, 0402	Murata Electronics	GRM155R71H102KA01D
C3	68 pF	CAP, 5%, 50V, C0G, 0402	Murata Electronics	GRM1555C1H680JA01D
C4, C5	100 pF	CAP, 5%, 50V, C0G, 0402	Murata Electronics	GRM1555C1H101JA01D
R1	0 Ω	RES, 0 Ω , 1210	KOA Speer Electronics	RK73Z2ETTD
L1	33 nH	IND, 5%, M/L, 0603	Murata Electronics	LL1608-FSL33NJ
J1, J2	n/a	CONN, SMA, EL, FLT, 0.068" SPE-000318	Amphenol RF Asia Corp	901-10426
U1	n/a	MMIC Amplifier	Qorvo	QPA4263C
J3, J4	n/a	CONN, HDR, ST, 1x2, 0.100", Hi-temp, T/H	Samtec Inc	HTSW-102-07-G-S

Component Values for Specific Frequencies

Frequency	500 MHz	850 MHz	1950 MHz	2400 MHz	3500 MHz
C4, C5	220 pF	100 pF	68 pF	56 pF	39 pF
C3	100 pF	68 pF	22 pF	22 pF	15 pF
L1	68 nH	33 nH	22 nH	18 nH	15 nH

Typical Performance – 850 MHz Application Circuit

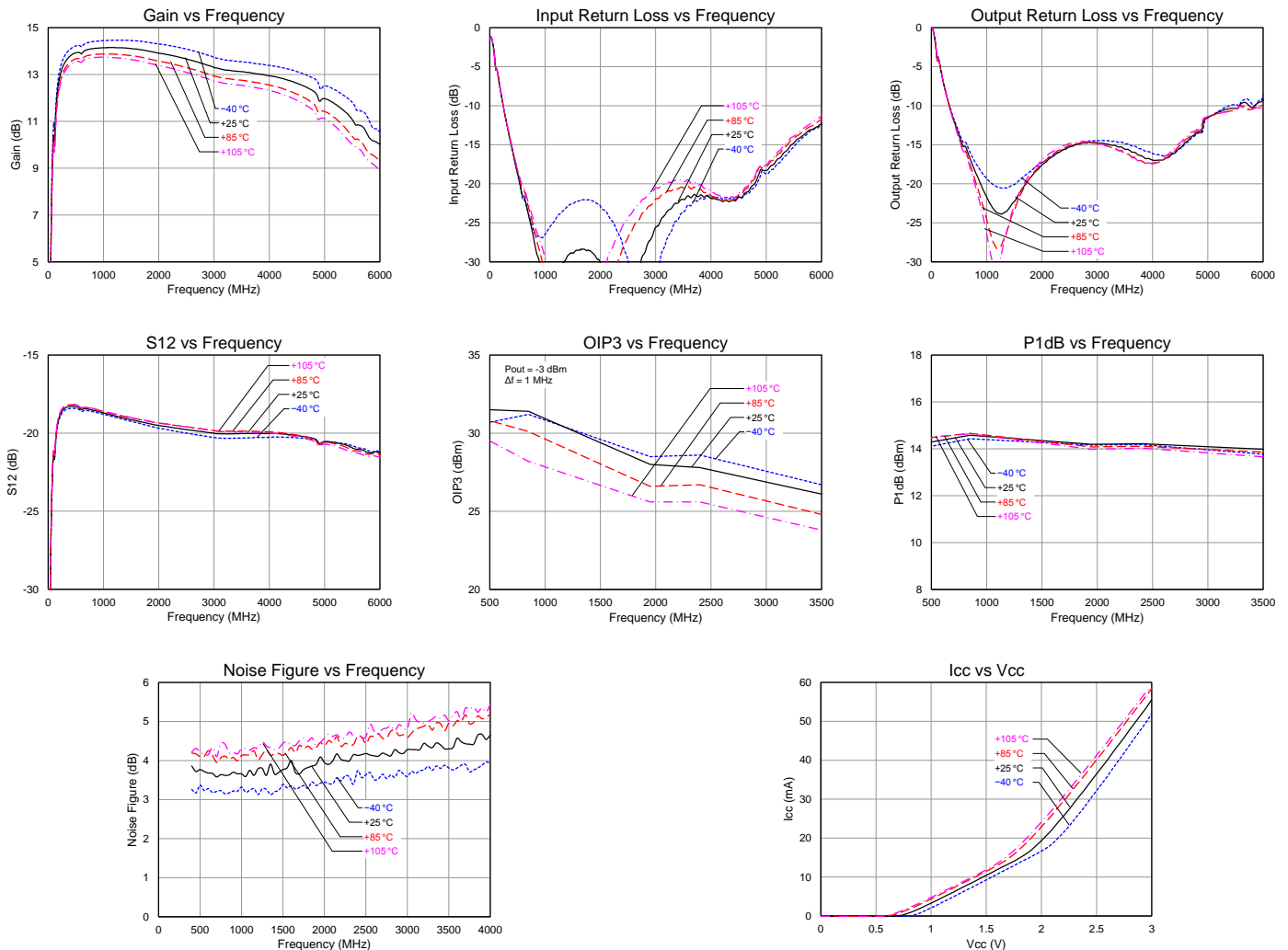
Parameter	Typical Value					Units
Frequency	500	850	1950	2400	3500	MHz
Small Signal Gain	13.9	14.1	13.9	13.7	13.1	dB
Output Third Intercept Point	+31.5	+31.4	+28	+27.8	+26.1	dBm
Output Power at 1dB Compression	+14.3	+14.6	+14.2	+14.2	+14	dBm
Input Return Loss	-18	-28	-30	-36	-22	dB
Output Return Loss	-14	-19	-18	-15.6	-15.5	dB
Reverse Isolation	-18.3	-18.5	-19.5	-19.7	-20	dB
Noise Figure	3.8	3.6	4.1	4.1	4.3	dB

Notes:

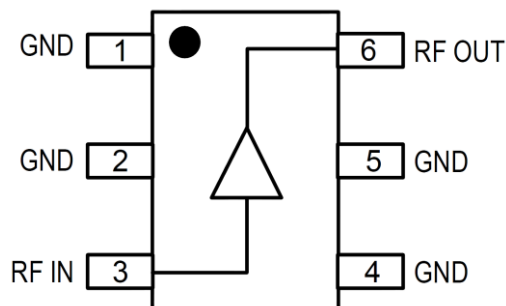
- Test conditions unless otherwise noted: $V_D = +3.0$ V, $I_D = 55$ mA Typ., OIP3 Tone Spacing=1 MHz, P_{OUT} per tone = -6 dBm, 50 Ω system, Temp = +25 °C

Performance Plots – 850 MHz Application Circuit

Test conditions unless otherwise noted: $V_D = +3.0$ V, $I_D = 55$ mA, OIP3 Tone Spacing=1 MHz, P_{OUT} per tone = -6 dBm, Temp.= +25 °C



Pin Configuration and Description

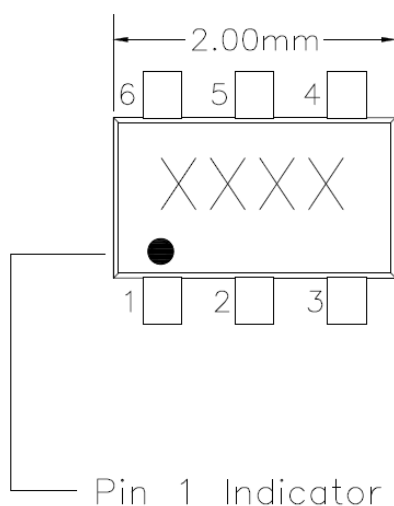


Top View

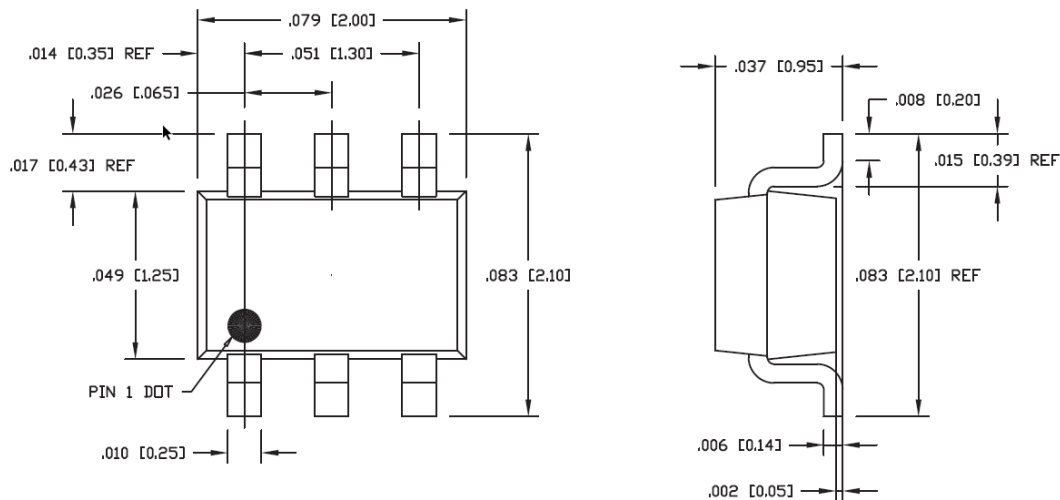
Pad No.	Label	Description
1, 2, 4, 5	GND	Connection to ground. Use via holes in PCB for best performance to reduce lead inductance as close to ground leads as possible
3	RF _{IN}	RF Input Pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
6	RF _{OUT}	RF Output and Bias Pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Package Marking

XXXX: Alpha-numeric Trace Code



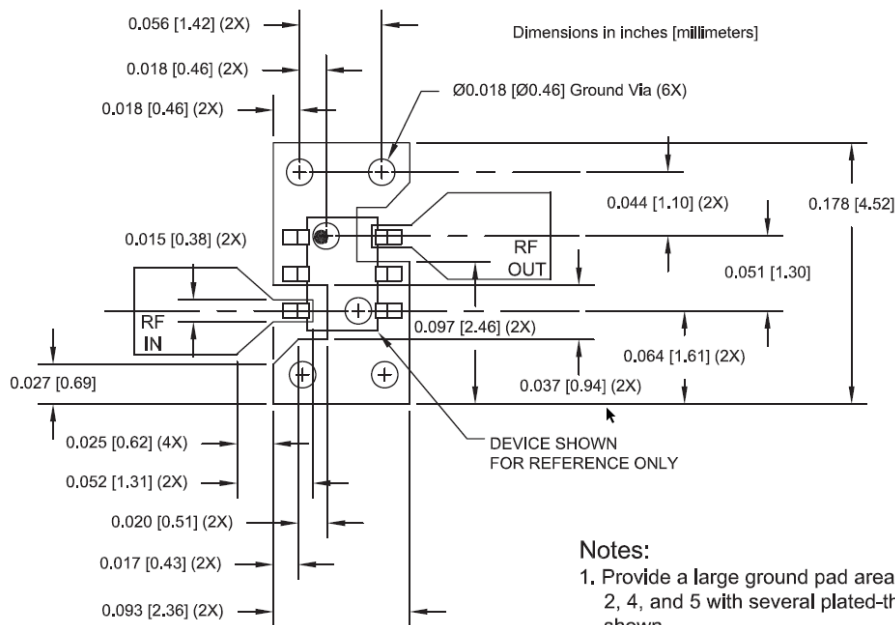
Package Dimensions



Notes:

1. All dimensions are in inches (millimeters). Angles are in degrees.
2. Dimension and tolerance formats conform to ASME Y15.5-2009.

PCB Mounting Pattern



Notes:

1. Provide a large ground pad area under device pins 1, 2, 4, and 5 with several plated-through holes placed as shown.
2. 1/2 ounce finished copper thickness is recommended.
3. RF I/O lines are 50Ω

Notes:

1. All dimensions are in inches (millimeters). Angles are in degrees.

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	2	ESDA / JEDEC JS-001-2014
ESD – Charged Device Model (CDM)	C3	ESDA / JEDEC JS-002-2014
MSL – Moisture Sensitivity Level	Level 1	IPC/JEDEC J-STD-020



Caution!
ESD-Sensitive Device

Solderability

Compatible with lead-free (260°C max. reflow temp.) soldering process.
Solder profiles available upon request.
Contact plating: Matte Sn

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

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