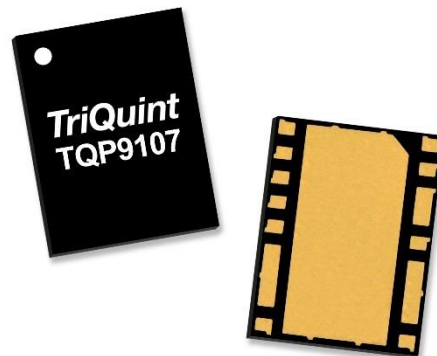


General Description

The TQP9107 is a high-efficiency two-stage power amplifier in a low-cost surface-mount package. The amplifier is able to achieve 31% power added efficiency at +27 dBm output power while operating with a low 84 mA idle current. The amplifier is designed to ensure that all odd-order IMD products are below -17 dBm at all output power levels below +24 dBm/ton.

The TQP9107 integrates two high performance amplifier stages onto a module to allow for a compact system design and requires very few external components for operation. The amplifier is bias adjustable allowing the amplifier's power consumption to be optimized. The TQP9107 is available in a lead-free/RoHS-compliant 13-pin 3.5 x 4.5mm surface mount package and is pin-compatible to the higher frequency band version in the family with the TQP9108 (1.7-2.17 GHz).

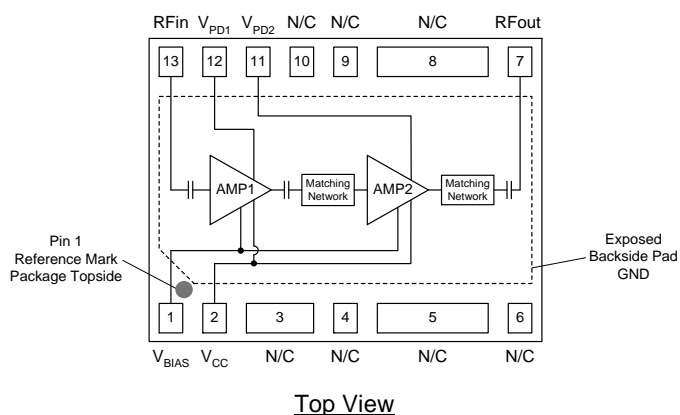


3.5 x 4.5 mm Leadless SMT Package

Product Features

- 600 – 960 MHz Frequency Range
- 35.5 dB gain
- +46 dBm Output IP3 at Pout = +24 dBm/ton
- 31% PAE at +27 dBm Pout
- Internally Matched
- Integrated Inter-Stage Matching
- Bias Adjustable
- Low idle current

Functional Block Diagram



Applications

- Wireless Infrastructure
- Repeaters, Boosters, DAS
- High Power Amplifiers
- Small cell BTS

Ordering Information

Part No.	Description
TQP9107	600 – 960 MHz Power Amplifier
TQP9107-PCB	Evaluation Board

Absolute Maximum Ratings

Parameter	Rating
Storage Temperature	-55 to +150 °C
RF Input Power, CW, 50 Ω, T=25 °C	+7 dBm
Supply Voltage (V _{CC})	+6 V

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
V _{CC}		+4.3	+5.0	V
V _{BIAS}		+3.9	V _{CC}	V
V _{PD1} , V _{PD2}		+4.0	V _{CC}	V
T _{CASE}	-40		+105	°C
T _j for >10 ⁶ hours MTTF			+170	°C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

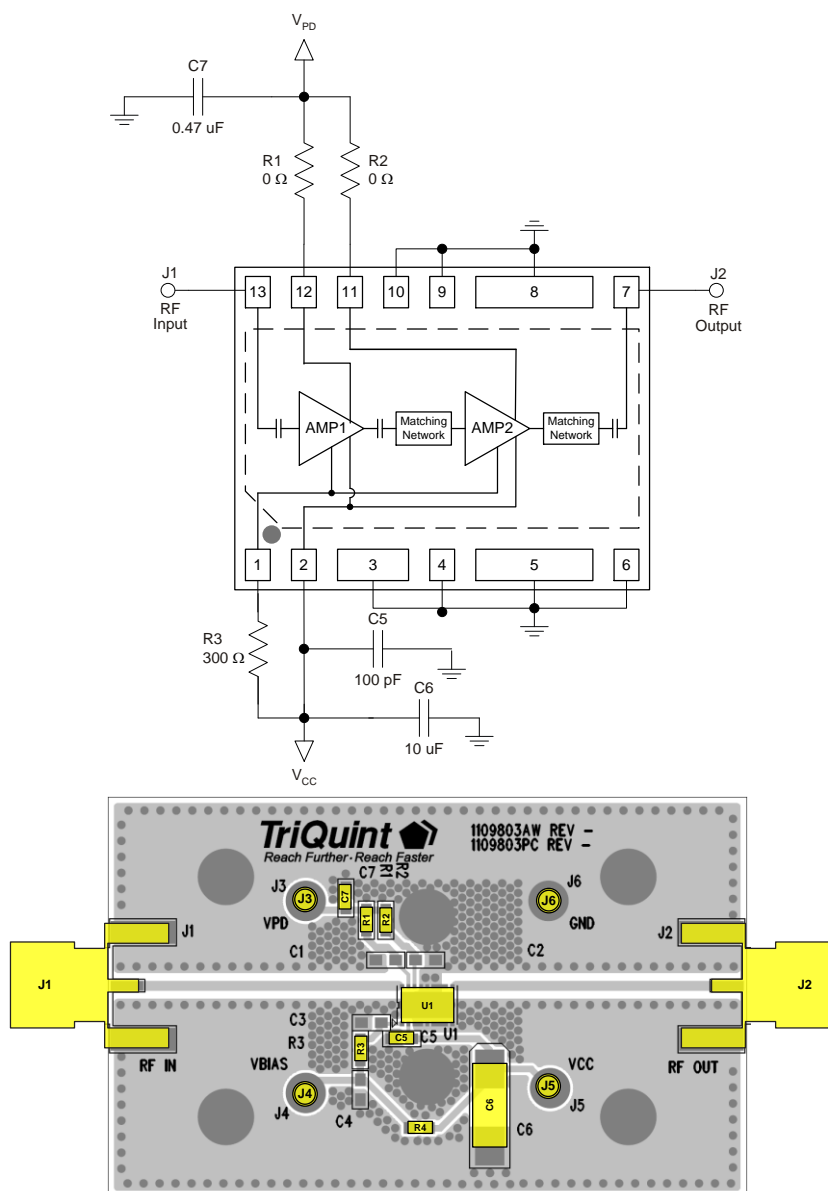
Electrical Specifications

Parameter	Conditions ⁽¹⁾	Min	Typ	Max	Units
Operational Frequency Range		600		960	MHz
Test Frequency			849		MHz
Gain		28.8	35.5		dB
Noise Figure			5		dB
Output P1dB			30.5		dBm
Output IP3	P _{out} = +23 dBm/tone, Δf = 600 kHz	+43	+46		dBm
IMD3, IMD5, IMD7	All power levels ≤ 23 dBm / tone			-17	dBm
Current, I _{CC}	P _{out} = +27 dBm		370		mA
Power Added Efficiency	P _{out} = +27 dBm		31		%
Idle Current	No RF Input Power		84		mA
VSWR Survivability	P _{out} = P1dB Signal: CW All Phases	10:1			–
VSWR Survivability	P _{out} = +28 dBm Signal: 20 MHz LTE 1C, PAR = 9.5 dB All Phases	6:1			–
Thermal Resistance, θ _{JC}	Junction to case		30.1		°C/W

Notes:

1. Test conditions unless otherwise noted: V_{CC} = +4.3 V, V_{PD1} = V_{PD2} = +4.0 V, Temp = +25 °C, 50 Ω system.

Application Circuit Schematic and Layout – TQP9107-PCB : 700 MHz to 960 MHz



Bill of Material – TQP9107-PCB : 700 MHz to 960 MHz

Reference Des.	Value	Description	Manuf.	Part Number
n/a		Nelco FR4, PCB APP BOARD	Qorvo	1109803PC
U1		2-Stage Power Amplifier	Qorvo	TQP9107
C5	100 pF	CAP, 0603, 5%, 50V, NPO	various	
C6	10 uF	CAP, 6032, 20%, 50V, Tantalum	various	
C7	0.47 uF	CAP, 0603, 50V, X7R, 5%	various	
R1, R2, R4	0 Ω	RES, 0603, 5%, 1/16W, Chip	various	
R3	300 Ω	RES, 0603, 5%, 1/16W, Chip	various	

Typical Performance – TQP9107-PCB : 700 MHz to 960 MHz

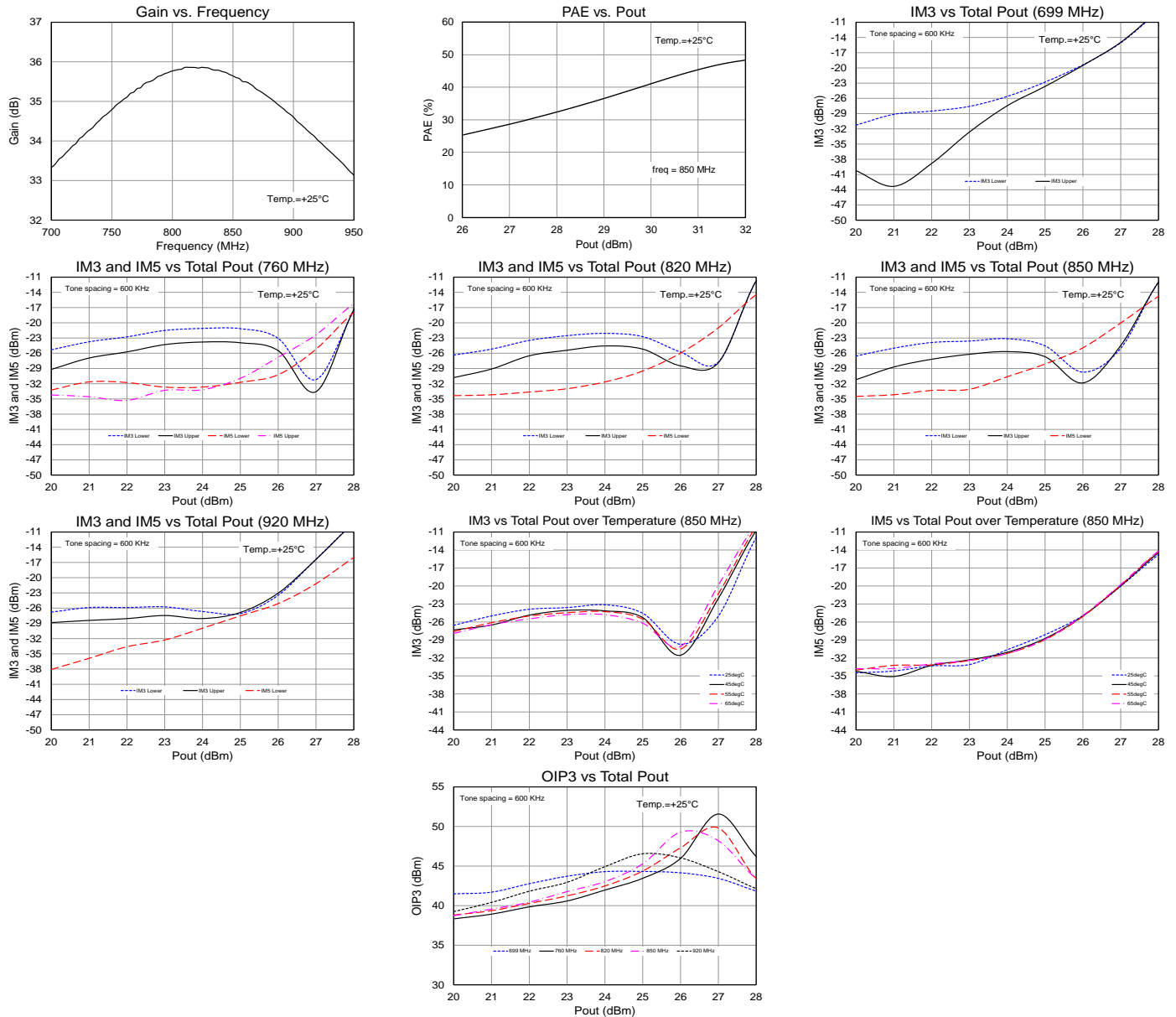
Parameter	Conditions ⁽¹⁾	Typical Value				Units
Frequency		760	820	850	920	MHz
Gain		35.0	35.8	35.7	34.1	dB
IM3	Pout= +27 dBm, Δf = 600 KHz	-30	-27	-24	-17	dBm
IM5	Pout= +27 dBm, Δf = 600 KHz	-22	-21	-20	-21	dBm
Quiescent Collector Current, I _{CQ}	V _{PD} = +4 V	84				mA

Notes:

1. Test conditions unless otherwise noted: V_{CC} = +4.3 V, V_{PD1} = V_{PD2} = +4.0V, Temp.=+25 °C

Performance Plots – TQP9107-PCB : 700 MHz to 960 MHz

Test conditions unless otherwise noted: V_{CC} = +4.3 V, V_{PD1} = V_{PD2} = +4.0V, Temp.=+25 °C



Typical Performance – $V_{CC} = +5.0\text{ V}$

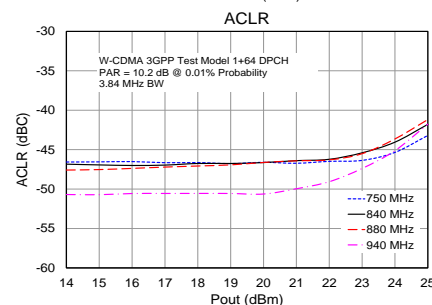
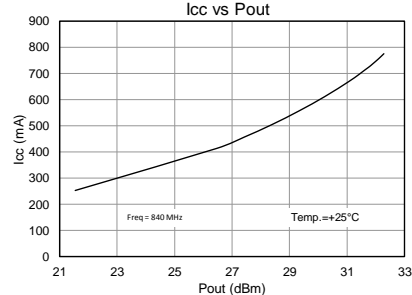
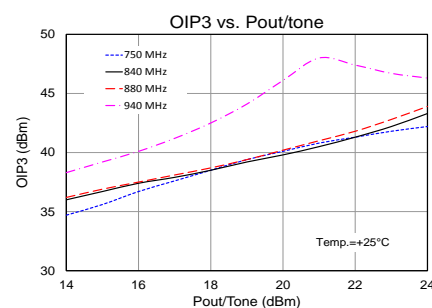
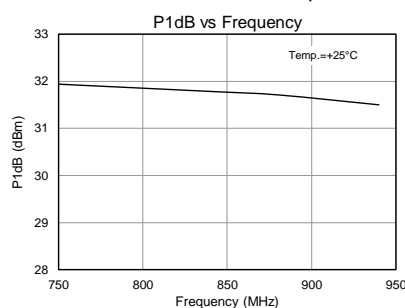
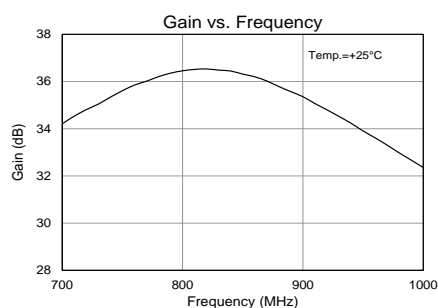
Parameter	Conditions ⁽¹⁾	Typical Value				Units
Frequency		750	840	880	940	MHz
Gain		35.5	36.2	35.8	34.0	dB
Input Return Loss		-13	-9	-9	-10	dB
Output P1dB		+31.9	+31.7	+31.6	+31.5	dBm
Output IP3	$P_{out} = +24\text{ dBm}$, $f = 1\text{ MHz}$	+42.1	+43.5	+44.0	+46.2	dBm
Quiescent Collector Current, I_{CQ}	$V_{PD} = +4.3\text{ V}$	108				mA

Notes:

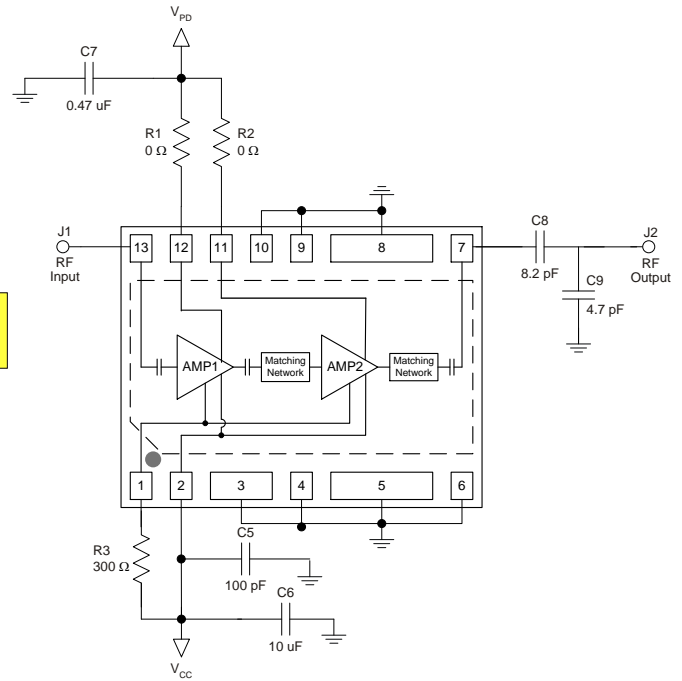
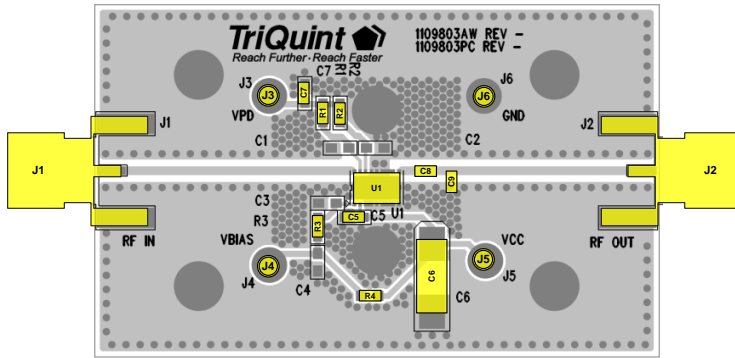
- Test conditions unless otherwise noted: $V_{CC} = +5.0\text{ V}$, $V_{PD1} = V_{PD2} = +4.3\text{ V}$, Temp. = +25 °C

Performance Plots – $V_{CC} = +5.0\text{ V}$

Test conditions unless otherwise noted: $V_{CC} = +5.0\text{ V}$, $V_{PD1} = V_{PD2} = +4.3\text{ V}$, Temp. = +25 °C



Evaluation Board and Schematic – 600 to 650 MHz



Notes:

1. All components are of 0603 size unless stated on the schematic.
2. The recommended component values are dependent upon the frequency of operation.
3. Critical component placement locations:
 - Distance between U1 (right edge) to C8 (left edge): 60 mil
 - Distance between U1 (right edge) to C9 (left edge): 160 mil

Bill of Material – 600 to 650MHz

Reference Des.	Value	Description	Manuf.	Part Number
n/a		Nelco FR4, PCB APP BOARD	Qorvo	1109803PC
U1		2-Stage Power Amplifier	Qorvo	TQP9107
C5	100 pF	CAP, 0603, 5%, 50V, NPO	various	
C6	10 uF	CAP, 6032, 20%, 50V, Tantalum	various	
C7	0.47 uF	CAP, 0603, 50V, X7R, 5%	various	
R1, R2, R4	0 Ω	RES, 0603, 5%, 1/16W, Chip	various	
R3	300 Ω	RES, 0603, 5%, 1/16W, Chip	various	
C8	8.2 pF	CAP, +/-0.1pF, 0603, 50V, COG	various	
C9	4.7 pF	CAP, +/-0.1pF, 0603, 50V, COG	various	

Typical Performance – 600 to 650 MHz

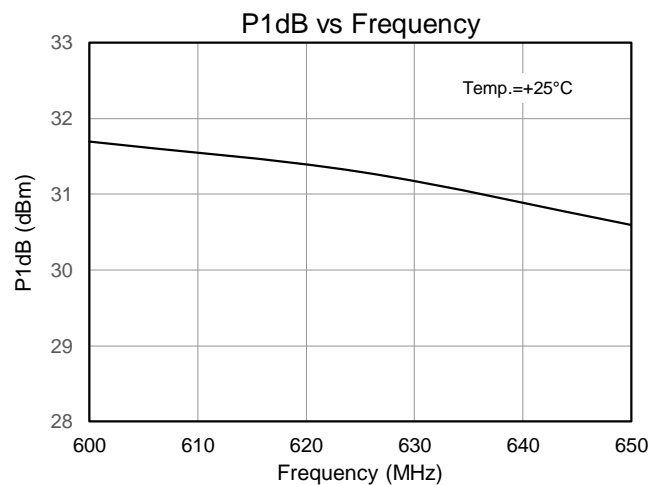
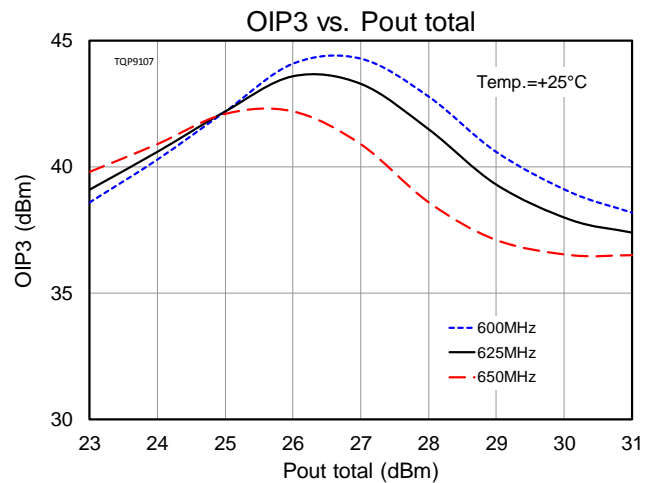
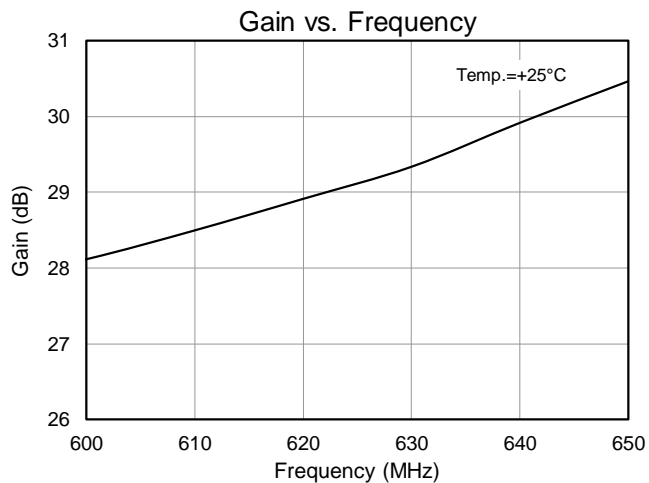
Parameter	Conditions	Typical Value			Units
Frequency		600	625	650	MHz
Gain		28.1	29.1	30.5	dB
Output P1dB		+31.7	+31.3	+30.6	dBm
Output IP3	Pout total = +26 dBm, $\Delta f = 1$ MHz	+44.1	+43.6	+42.2	dBm
Quiescent Collector Current, I_{CQ}		86			mA

Notes:

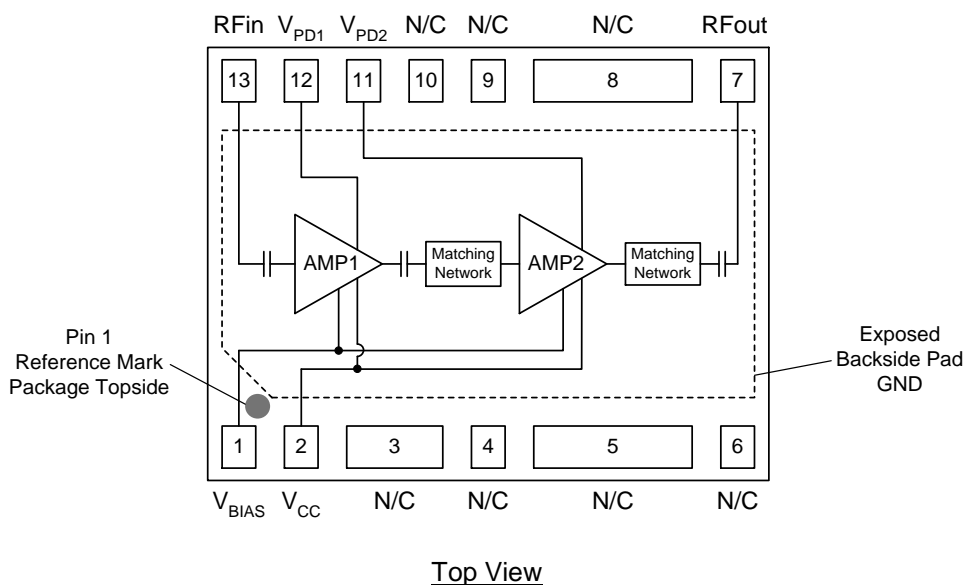
1. Test Conditions: $V_{CC} = +5.0$ V, $V_{BIAS} = +4.0$ V, Temp. = +25°C, 50Ω System

Performance Plot – 600 to 650 MHz

Test Conditions: $V_{CC} = +5.0$ V, $V_{BIAS} = +4.0$ V, Temp. = +25°C, 50Ω System



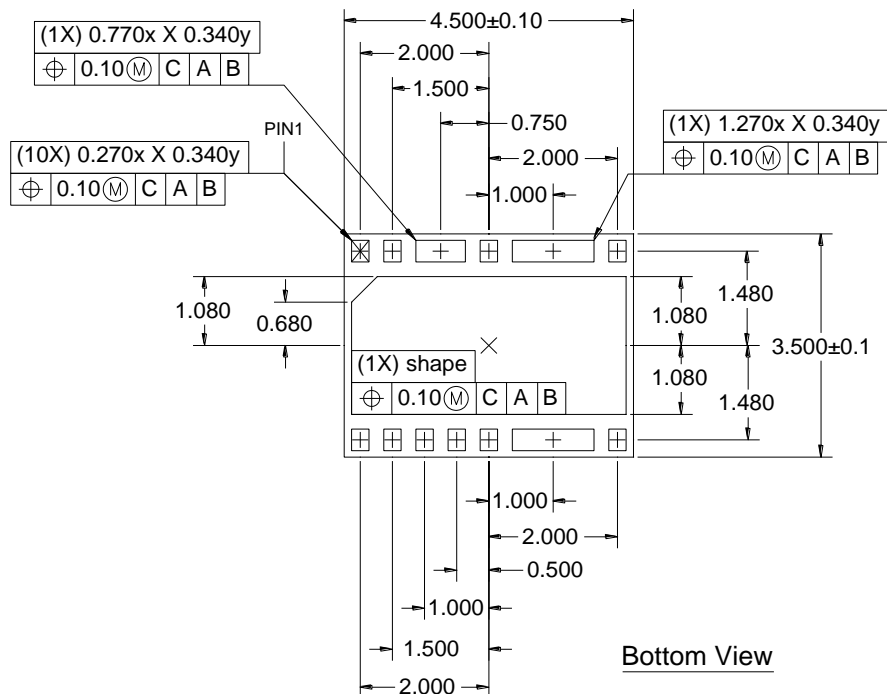
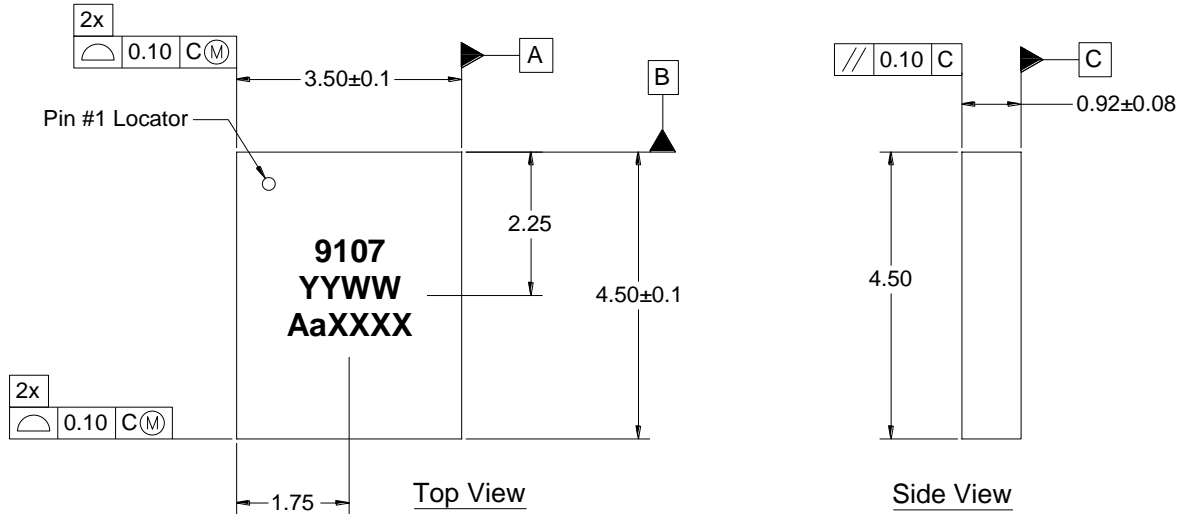
Pin Configuration and Description



Pad No.	Label	Description
1	V _{BIAS}	Provides reference voltage for internal active biasing circuit
2	V _{CC}	DC voltage supply connection
3, 4, 5, 6, 8, 9, 10	N/C	No internal connection. Provide grounded land pads for PCB mounting integrity.
7	RF out	RF output pin. The DC is internally blocked at this pin.
11	V _{PD1}	Power down for Amp 1. This voltage adjusts for the current draw in Amp 1.
12	V _{PD2}	Power down for Amp 2. This voltage adjusts for the current draw in Amp 2.
13	RF in	RF input pin. The DC is internally blocked at this pin.
Backside Pad	GND	RF/DC ground. Use recommended via pattern to minimize inductance and thermal resistance; see PCB Mounting Pattern for suggested footprint.

Package Marking and Dimensions

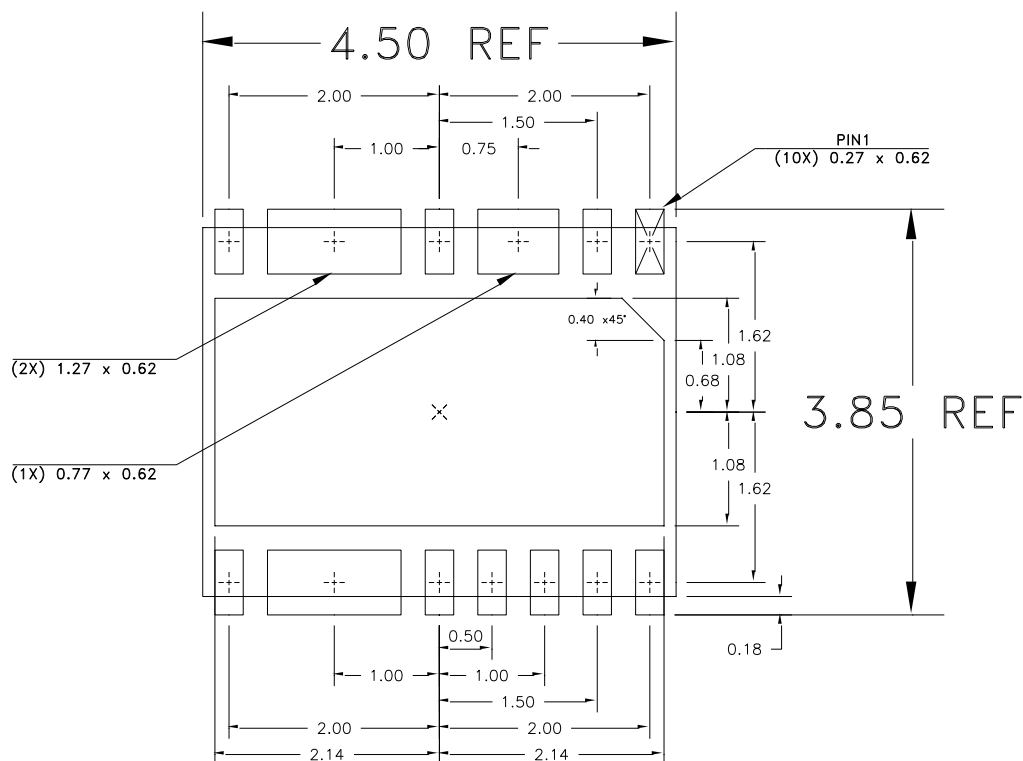
Marking: Part Number – TQP9107
 Date – YYWW
 Lot Code - AaXXXX



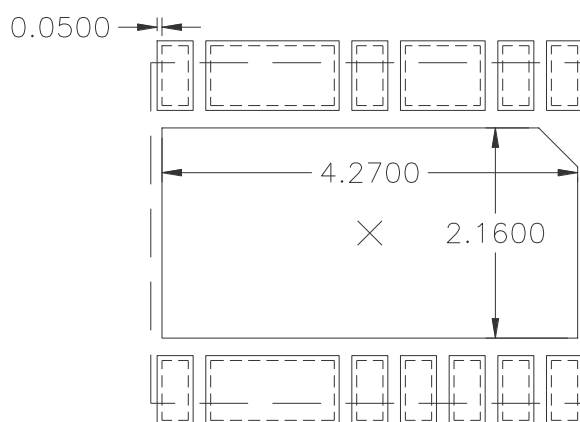
- Notes:
1. All dimensions are in millimeters. Angles are in degrees.
 2. The terminal #1 identifier and terminal numbering conform to JESD 95-1 SPP-012.
 3. Contact plating: Au over Ni

PCB Mounting Pattern

Recommend PCB land-pad pattern metallization (Top View)



Recommended PCB solder mask opening (Top View)



Notes:

1. A heat sink underneath the area of the PCB for the mounted device is strictly required for proper thermal operation. Damage to the device can occur without the use of one.
2. Ground / thermal vias are critical for the proper performance of this device. Vias should use a .35mm (#80 / .0135") diameter drill and have a final plated thru diameter of .25 mm (.010").
3. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1C	ESDA / JEDEC JS-001-2012
ESD – Charged Device Model (CDM)	Class C3	JEDEC JESD22-C101F
MSL – Moisture Sensitivity Level	Level 3	IPC/JEDEC J-STD-020



Caution!
ESD-Sensitive Device

Solderability

Compatible with both lead-free (260°C max. reflow temp.) and tin/lead (245°C max. reflow temp.) soldering processes.
Solder profiles available upon request.
Contact plating: Electrolytic plated Au over Ni

RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment). This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free
- Qorvo Green



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

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Web: www.qorvo.com

Email: customer.support@qorvo.com

For technical questions and application information: **Email:** appsupport@qorvo.com

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